FACULTY PROFILE

NameMr Patil Rohit VasantDesignationAssistant ProfessorDepartmentElectrical EngineeringMobile No.+919766250506e-mailrvp_ele@adcet.in

Educational Qualifications:

| Degree | Year of Passing | University | Grade |
|-----------------------------------|-----------------|-------------|-------------|
| B.E. (Electrical Engineering) | 2008 | WCE, Sangli | Distinction |
| M.Tech.(Electrical Power Systems) | 2012 | WCE, Sangli | Distinction |

GATE Qualified (2010)

Area of Specialization:

Electrical Power System, Smart Grid, Power System Stability, Power System Protection

Professional Experience:

| Teaching | Industrial | R & D | Total |
|----------|------------|-------|-------|
| 10 | 03 | 01 | 14 |

Research Publications:

| International Journals International Conferences | | National Journals | National Conferen | ces Total |
|--|----|--------------------------|-------------------|-----------|
| 01 | 02 | 01 | 01 | 05 |

STTP / Workshops / Seminars Attended:

| STTP | Workshops | Seminars |
|------------------|-----------|----------|
| 01 | 03 | 00 |
| Students Guided: | | |
| UG | PG | Ph.D. |
| 10 | 00 | 00 |

Collaboration with Industry:

Nil

Professional Memberships:

NIL

Special Achievements:

- 1. Published project paper for IEEE-EXPLORE, DIGITAL LIBRARY.
- 2. GATE qualified (2010) & also cracked CAT with 98%le.
- 3. Participated in IEEE-INTERNATIONAL LEVEL conferences SCEECS -2012 at NIT, Bhopal, Madhya Pradesha.
- 4. Participated in IEEE-INTERNATIONAL LEVEL conferences ICAESM-2012 at, Nagapattinam, Tamilnadu.
- 5. Manuscript has been Published in Volume 3 Issue 3 (June 2016 Issue) of IJTRD
- 6. Secured FIRST position in national level project competition at TATA POWER, MUMBAI.
- 7. Secured SECOND position in National level project presentation at VIT, Pune.
- 8. Participated in NATIONAL LEVEL conference ATEES -2012 at GIT, Belgaum.