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Forward Biasing

Forward Biased

- +ve terminal of battery connected with p-type
- -ve terminal of battery connected with n-type.
- Maximum current flow through the diode during forward biasing.
- The D.R is small in forward biased p-n junction

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- When anode in p-n junction is cathode, the holes are injected from anode side. It is called as forward biasing. In this region it is forward biasing.
- This is a unipolar carrier device.
- When anode gate voltage, anode-cathode voltage across the junction is forward biasing. It is forward biasing.
- It is a device that does not have reverse blocking.
- When gate voltage is zero, the MOSFET acts as a diode. It is forward biasing.
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- It is a device that does not have reverse blocking.
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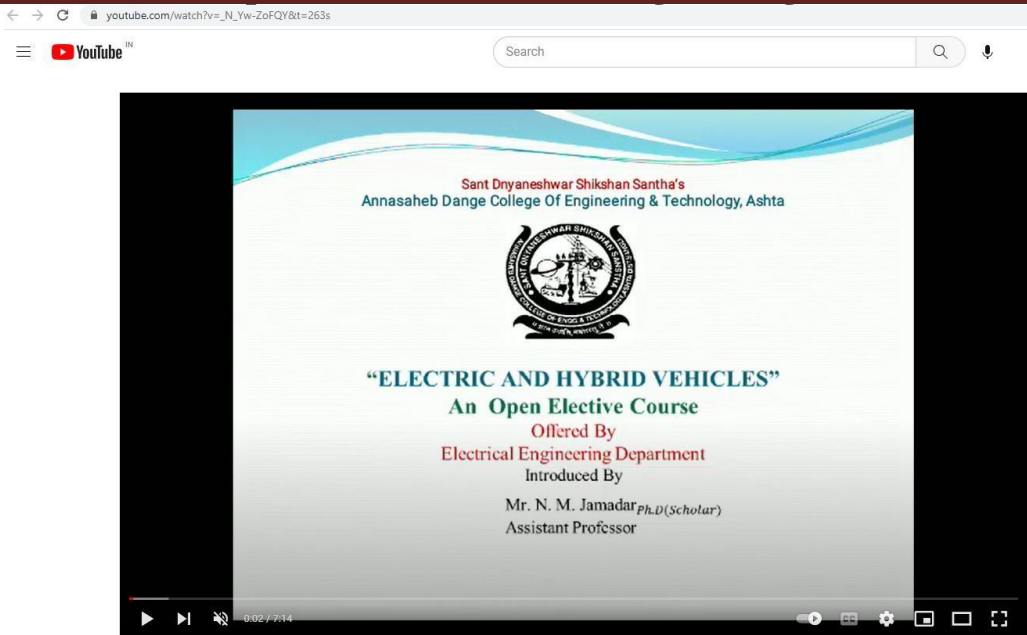
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- It is a device that does not have reverse blocking.
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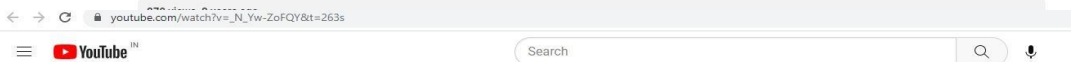
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Unit 3	Energy Storage and Regeneration Electrochemical batteries- Electrochemical reaction, thermodynamic voltage, specific energy, power, efficiency, applications of different battery technologies in EV and HEV Ultra capacitors- Features, Basic operating principle, Performance, applications of different ultra-capacitor technologies in EV and HEV Ultra high speed flywheels- operating principle, power capacity, applications of different flywheel technologies in EV and HEV Fundamentals of regenerative braking- Energy consumption in braking, braking power and energy on front and rear wheels, brake system for EV and HEV
Unit 4	Electric Vehicles (EV) Configurations of EV, Performance of EV, Traction motor characteristics, tractive effort and transmission requirement, vehicle performance, tractive effort in normal driving, energy consumption
Unit 5	Hybrid Electric Vehicles (HEV) Concept of hybrid electric drive trains, architecture of HEV drive trains, series hybrid, parallel hybrid- Torque coupling drive trains, speed coupling drive trains, speed and torque coupling drive trains.
Unit 6	Hybrid Drive Train Designs Series Hybrid Electric Drive Train Design- Operation patters, control strategies, PPS control, Thermostat control, Sizing of major components, power rating design of traction motor and engine, Design of Peaking Power Source (PPS) Parallel Hybrid Drive train design -Control strategies, State of charge (SOC) control, engine on-off control, Design of drive train parameters

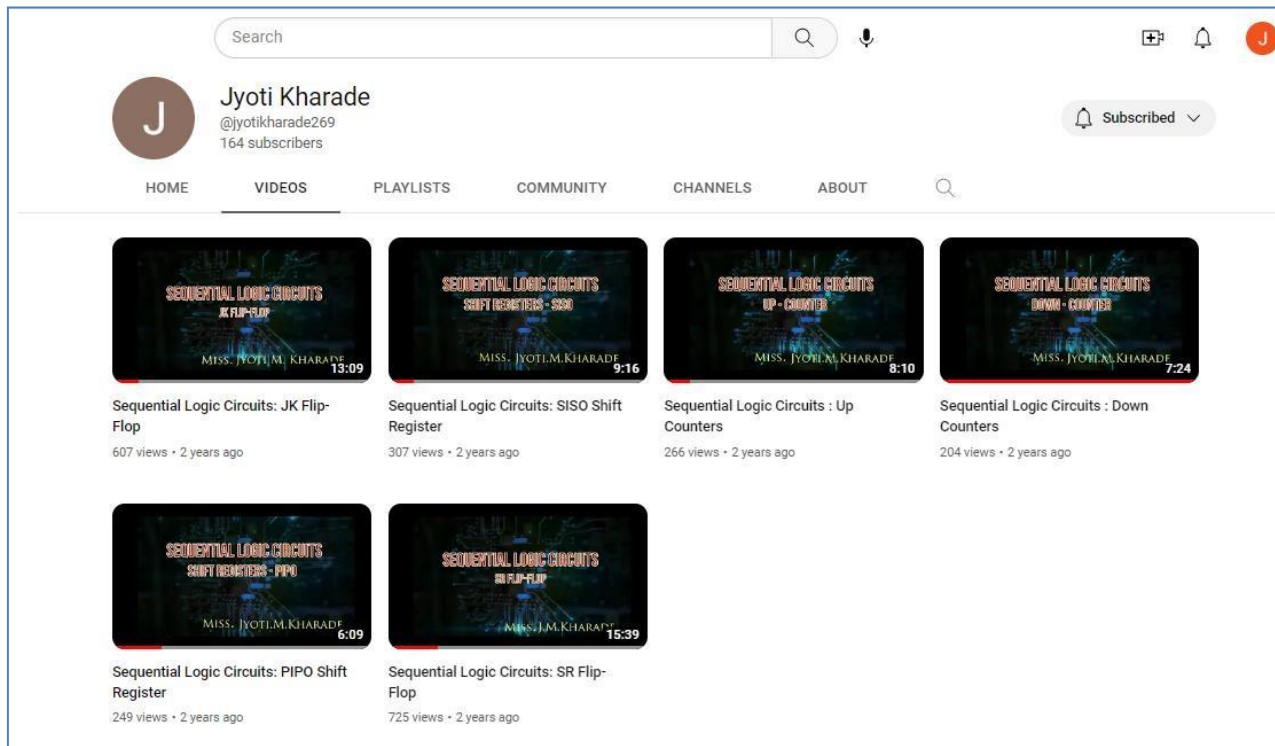
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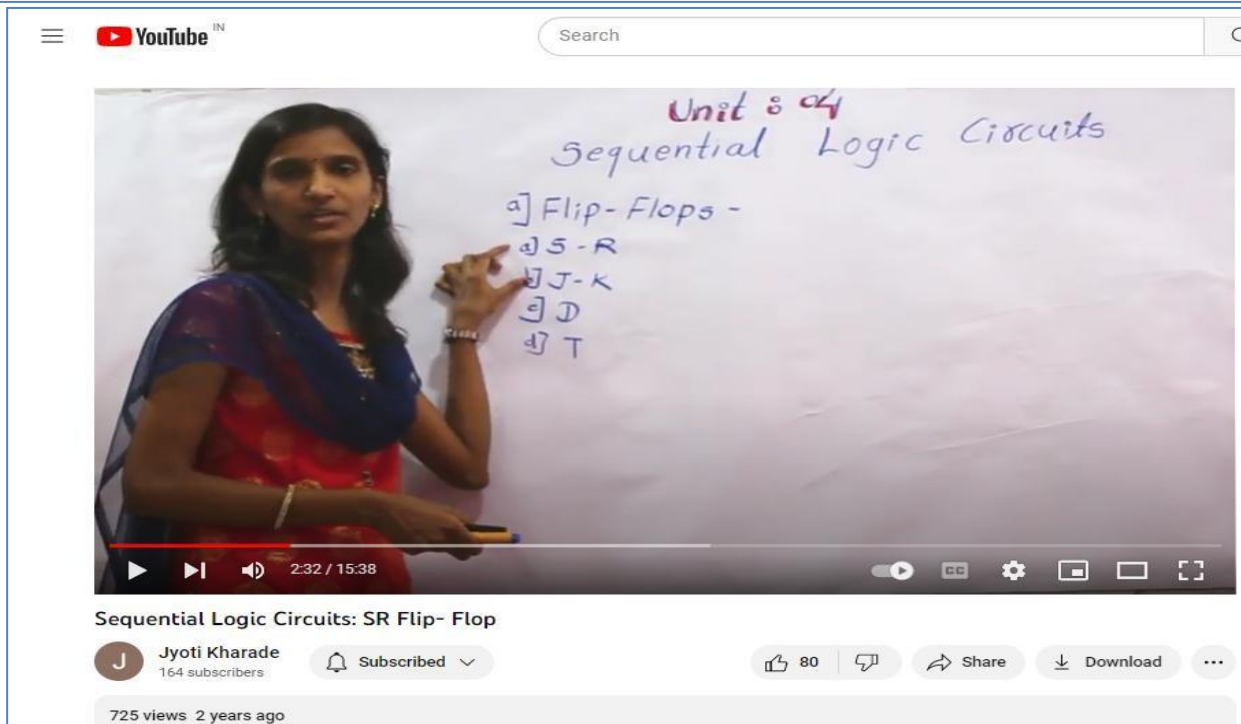
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Counter

A) Asynchronous (Ripple) Counter
 B) Synchronous Counter

a) Up Counter [0, 1, 2, ...]
 b) Down Counter [N, N-1, ...]

2-Bit Ripple-Up Counter:-

High JIP FF1 High JIP FF2

LSB MSB

CLK	Q ₂	Q ₁	Q ₀
0			
1			
2			
3			
4			

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JK - Flip-Flop

J K

CLK

Q

Q̄

J	K	R	S	Q	Q̄	State

Sequential Logic Circuits: JK Flip-Flop

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JK flip-flop is a sequential bi-state single-bit memory device named after its inventor by Jack Kilby. [Show more](#)

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